

GOODRAM Industrial 2.5" SSD S11 3D TLC silver-diamond

DATASHEET



Version: 1.3 Date: October 2020

2,5" Solid State Drive with SATA interface for Industrial Applications

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REVISION HISTORY

VERSION	CHANGES	DATE
1.0	Initial release	01.10.2019
1.1	Logo amendment	16.06.2020
1.2	P/N Toshiba/Kioxia amendment	14.08.2020
1.3	1TB P/N amendment	28.10.2020



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PRODUCT OVERVIEW

- Capacity
 - o 30GB up to 1024GB
- Controller
 - o PS3111-S11
- SATA Interface
- o SATA Revision 3.2
- o SATA 1.5Gbs, 3Gbps and 6Gbps interface
- Flash Memory
- o Flash Type: Kioxia 3D TLC
- o 1pcs to 4pcs of TSOP/BGA flash
- Performance Note1
- o Read: up to 550 MB/s
- o Write: up to 500 MB/s
- Power Consumption^{Note2}
- o Active mode: < 1,680mW
- o Idle mode: < 325mW
- o DEVSLP mode: <5mW
- TBW (terabytes written) Note3
 - o 835TBW for 1024GB

- MTBF
 - o More than 2,000,000 hours
- Advanced Flash Management
 - o Static and Dynamic Wear Leveling
 - o Bad Block Management
 - o TRIM
 - o SMART
 - o NCQ
 - o Over-provisioning
 - o Firmware update
 - o SmartZIP
- Low Power Management
 - o DIPM/HIPM Mode
 - o DEVSLP Mode (optional)
- Temperature Range Note4
 - o Operational (Silver): 0 ~+ 70°C
 - o (Diamond): -40° C $\sim +85^{\circ}$ C
 - o Storage: -40°C ~ +85°C
- RoHS compliant

Notes:

- 1. Measured by CrystalDiskMark v3.0
- 2. Please see "Power Consumption" for details.
- 3. Please see "TBW (Terabyte Written)" for details.
- 4. According to standards IEC-60068-2-1/2/14/38

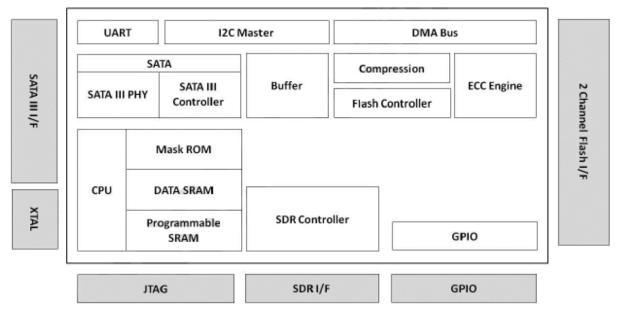


PRODUCT DETAILS

GENERAL DESCRIPTION

GOODRAM Industrial 2.5" SSD delivers all the advantages of Flash Drive technology with Serial ATA III interface. The SATA SSD is designed to operate at a maximum operating frequency of 200MHz with 30MHz external crystal. The capacity could provide a wide range up to 1024GB and the performance reach up to 550MB/s read as well as 500MB/s write based on Toggle 2.0 MLC (with 32MB SDR cache enabled and measured by CrystalDiskMark). Meanwhile, the power consumption is much lower than traditional Hard Drives.

CONTROLLER BLOCK DIAGRAM



PS3111-S11 2.5" SATA SSD Controller Block Diagram

FLASH MANAGEMENT



GOODRAM Industrial 2.5" MLC SSD utilizes all the state of art technologies to ensure full reliability until the TBW parameter is reached. These technologies include:

Error Correction Code (ECC)

Flash memory cells will deteriorate with use, which might generate random bit errors in the stored data. Thus, SSD drive applies the LDPC (Low Density parity Check) of ECC algorithm, which can detect and correct errors occur during read process, ensure data been read correctly, as well as protect data from corruption.

Wear Leveling

NAND Flash devices can only undergo a limited number of program/erase cycles, and in most cases, the flash media are not used evenly. If some area get updated more frequently than others, the lifetime of the device would be reduced significantly. Thus, Wear Leveling technique is applied to extend the lifespan of NAND Flash by evenly distributing write and erase cycles across the media. Product has advanced Wear Leveling algorithm, which can efficiently spread out the flash usage through the whole flash media area. Moreover, by implementing both dynamic and static Wear Leveling algorithms, the life expectancy of the NAND Flash is greatly improved.

Bad Block Management

Bad blocks are blocks that include one or more invalid bits, and their reliability is not guaranteed. Blocks that are identified and marked as bad by the manufacturer are referred to as "Initial Bad Blocks". Bad blocks that are developed during the lifespan of the flash are named "Later Bad Blocks". We implement an efficient bad block management algorithm to detect the factory-produced bad blocks and manages any bad blocks that appear with use. This practice further prevents data being stored into bad blocks and improves the data reliability.

TRIM

TRIM is a feature which helps improve the read/write performance and speed of solid-state drives (SSD). Unlike hard disk drives (HDD), SSDs are not able to overwrite existing data, so the available space gradually becomes smaller with each use. With the TRIM command, the operating system can inform the SSD which blocks of data are no longer in use and can be removed permanently. Thus, the SSD will perform the erase action, which prevents unused data from occupying blocks all the time.

SMART



SMART, an acronym for Self-Monitoring, Analysis and Reporting Technology, is an open standard that allows a hard disk drive to automatically detect its health and report potential failures. When a failure is recorded by SMART, users can choose to replace the drive to prevent unexpected outage or data loss. Moreover, SMART can inform users of impending failures while there is still time to perform proactive actions, such as copy data to another device.

Over-Provision

Over Provisioning refers to the inclusion of extra NAND capacity in a SSD, which is not visible and cannot be used by users. With Over Provisioning, the performance and IOPS (Input/Output Operations per Second) is improved by providing the controller additional space to manage P/E cycles, which enhances the reliability and endurance as well. Moreover, the write amplification of the SSD becomes lower when the controller writes data to the flash.

Firmware Upgrade

Firmware can be considered as a set of instructions on how the device communicates with the host. Firmware will be upgraded when new features are added, compatibility issues are fixed or read/write performance gets improved.

ADDITIONAL FEATURES

Low Power Management (DIPM/HIPM Mode)

SATA interfaces contain two low power management states for power saving: Partial and Slumber modes. For Partial mode, the device has to resume to full operation within 10 microseconds, whereas the device will spend 10 milliseconds to become fully operational in the Slumber mode. SATA interfaces allow low power modes to be initiated by Host (HIPM, Host Initiated Power Management) or Device (DIPM, Device Initiated Power Management). As for HIPM, Partial or Slumber mode can be invoked directly by the software. For DIPM, the device will send requests to enter Partial or Slumber mode.

DEVSLP Mode (optional)

With the increasing need of aggressive power/battery life, SATA interfaces include a new feature, Device Sleep (DEVSLP) mode, which helps further reduce the power consumption of the device. DEVSLP enables the device to completely power down the device PHY and other sub-systems, making the device reach a new level of lower power operation. The DEVSLP does not specify the exact power level a device can achieve in the DEVSLP mode, but the power usage can be dropped down to 5mW or less.



Power Loss Protection: Flushing Mechanism

Power Loss Protection is a mechanism to prevent data loss during unexpected power failure. DRAM is a volatile memory and frequently used as temporary cache or buffer between the controller and the NAND flash to improve the SSD performance. However, one major concern of the DRAM is that it is not able to keep data during power failure. Accordingly, the controller applies the Guaranteed Flush Technology, which requests the controller to transfer data to the cache. For the used controller, SDR performs as a cache, and its sizes include up to 32MB. Only when the data is fully committed to the NAND flash will the controller send acknowledgement (ACK) to the host. Such implementation can prevent false-positive performance and the risk of power cycling issues. Additionally, it is critical for a controller to shorten the time the in-flight data stays in the cache. Thus, the controller applies an algorithm to reduce the amount of data resides in the cache to provide a better performance. This SmartCacheFlush technology allows incoming data to only have a "pit stop" in the cache and then move to the NAND flash at once. If the flash is jammed due to particular file sizes (random 4K), the cache will be treated as an "organizer", consolidating incoming data into groups before written into the flash to improve write amplification. In sum, with this Flush Management, the controller proves to provide the reliability required by consumer, industrial, and enterprise-level application.

Advanced Device Security Features (Secure Erase, Write Protect)

Secure Erase is a standard ATA command and will write all "0xFF" to fully wipe all the data on hard drives and SSDs. When this command is issued, the SSD controller will empty its storage blocks and return to its factory default settings. When a SSD contains too many bad blocks and data are continuously written in, then the SSD might not be used anymore. Thus, Write Protect is a mechanism to prevent data from being written in and protect the accuracy of data that are already stored in the SSD.

$SmartZIP^{TM}$

Write data to the NAND Flash costs time. To improve the write speed performance, controller launches with compression technique – SmartZIPTM. Whether a file could be compressed or not depending on the file type, for file types have redundancy data pattern, through our embedded encode engine, we could reduce the amount of data that is actually written to the Flash. Comparing to the SSD without compression, write efficiency is raised and the SSD endurance is also improved since Flash could be benefit from less data written for longer SSD lifetime.



PERFORMANCE AND POWER CONSUMPTION

		Perfo	Performance		Power Consumption			
Capacity	Flash	Crystall	6	\A/:::+ -	1.11.	חבייטו		
Capacity	Structure	Read	Write	Read (mW)	Write (mW)	Idle (mW)	DEVSLP (mW)	
		(MB/s)	(MB/s)	(11111)	(11144)		(11111)	
30/32GB	32GB x 1, TSOP	300	125	1,100	1,000	325	4.9	
60/64GB	32GB x 2, TSOP	550 255 3		1,230	1,020	320	4.9	
120/128GB	32GB x 4, TSOP	550	550 450		1,350	320	4.9	
240/256GB	128GB x 2, BGA	550	490	1,350	1,450	325	4.9	
480/512GB	128GB x 4, BGA	550 490		1,470	1,670	320	4.9	
960/1024GB	256GB x 4, BGA	550	500	1,575	1,680	320	4.9	

NOTES:

- 1. The performance was measured using CrystalDiskMark with SATA 6Gbps host.
- 2. Samples were built using Kioxia BiCS3 TLC NAND flash.
- 3. Performance may differ according to flash configuration and platform.
- 4. The table above is for reference only. The criteria for MP (mass production) and for accepting goods shall be discussed based on different flash configuration.

SUPPLY VOLTAGE

PARAMETER	RATING			
Operating voltage	5V +/- 5%			

TBW

Capacity	Flash Structure	TBW
30/32GB	32GB x 1, TSOP	17
60/64GB	32GB x 2, TSOP	42
120/128GB	32GB x 4, TSOP	75
240/256GB	128GB x 2, BGA	480
480/512GB	128GB x 4, BGA	425
960/1024GB	256GB x 4, BGA	835

NOTES:

- 1. Samples were built using Kioxia MLC NAND flash.
- 2. The test followed JEDEC219A client endurance workload.
- 3. TBW may differ according to flash configuration and platform.
- 4. The endurance of SSD could be estimated based on user behaviour, NAND endurance cycles, and write amplification factor. It is not guaranteed by flash vendor.



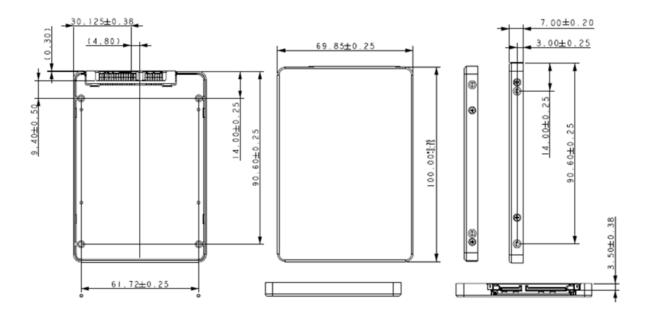
PRODUCT ORDERING INFORMATION

PN	Type	Capacity	Technology	Temp range	Grade
RUS27T030S3SB-P11KID	2,5" SATA	30GB	3D TLC	0°C~+70°C	silver
RUS27T032S3SB-P11KID	2,5" SATA	32GB	3D TLC	0°C~+70°C	silver
RUS27T060S3SB-P11KID	2,5" SATA	60GB	3D TLC	0°C~+70°C	silver
RUS27T064S3SB-P11KID	2,5" SATA	64GB	3D TLC	0°C~+70°C	silver
RUS27T120S3SB-P11KID	2,5" SATA	120GB	3D TLC	0°C~+70°C	silver
RUS27T128S3SB-P11KID	2,5" SATA	128GB	3D TLC	0°C~+70°C	silver
RUS27T240S3SB-P11KID	2,5" SATA	240GB	3D TLC	0°C~+70°C	silver
RUS27T256S3SB-P11KID	2,5" SATA	256GB	3D TLC	0°C~+70°C	silver
RUS27T480S3SB-P11KID	2,5" SATA	480GB	3D TLC	0°C~+70°C	silver
RUS27T512S3SB-P11KID	2,5" SATA	512GB	3D TLC	0°C~+70°C	silver
RUS27T960S3SB-P11KID	2,5" SATA	960GB	3D TLC	0°C~+70°C	silver
RUS27T01TS3SB-P11KID	2,5" SATA	1024GB	3D TLC	0°C~+70°C	silver
RUS27T030S3DB-P11KID	2,5" SATA	30GB	3D TLC	-40°C~+85°C	diamond
RUS27T032S3DB-P11KID	2,5" SATA	32GB	3D TLC	-40°C~+85°C	diamond
RUS27T060S3DB-P11KID	2,5" SATA	60GB	3D TLC	-40°C~+85°C	diamond
RUS27T064S3DB-P11KID	2,5" SATA	64GB	3D TLC	-40°C~+85°C	diamond
RUS27T120S3DB-P11KID	2,5" SATA	120GB	3D TLC	-40°C~+85°C	diamond
RUS27T128S3DB-P11KID	2,5" SATA	128GB	3D TLC	-40°C~+85°C	diamond
RUS27T240S3DB-P11KID	2,5" SATA	240GB	3D TLC	-40°C~+85°C	diamond
RUS27T256S3DB-P11KID	2,5" SATA	256GB	3D TLC	-40°C~+85°C	diamond
RUS27T480S3DB-P11KID	2,5" SATA	480GB	3D TLC	-40°C~+85°C	diamond
RUS27T512S3DB-P11KID	2,5" SATA	512GB	3D TLC	-40°C~+85°C	diamond
RUS27T960S3DB-P11KID	2,5" SATA	960GB	3D TLC	-40°C~+85°C	diamond
RUS27T01TS3DB-P11KID	2,5" SATA	1024GB	3D TLC	-40°C~+85°C	diamond

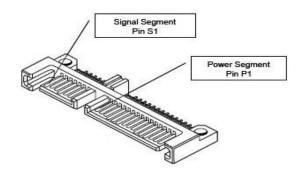


PHYSICAL DIMENSION

Dimension: 100.00mm (L) x 69.85mm (W) x 7.00mm (H)



PIN ASSIGNMENT AND DESCRIPTIONS



Signal Segment Pin Assignment and Descriptions

Pin Number	Function
S1	GND
S2	A+ (Differential Signal Pair A)
S3	A – (Differential Signal Pair A)
S4	GND
S5	B – (Differential Signal Pair B)
S6	B+ (Differential Signal Pair B)
S7	GND



Power Segment Pin Assignment and Descriptions

Pin Number	Function
P1	Not Used (3.3V)
P2	Not Used (3.3V)
P3	DEVSLP
P4	GND
P5	GND
P6	GND
P7	5V pre-charge
P8	5V
P9	5V
P10	GND
P11	Reserved
P12	GND
P13	Not Used (12V pre-charge)
P14	Not Used (12V)
P15	Not Used (12V)

SUPPORTED ATA COMMAND LIST

Op-Code	Command Description	Op-Cod	de	Command Description
00h	NOP	C9h		Read DMA without Retry
06h	Data Set Management	CAh		Write DMA
10h-1Fh	Recalibrate	CBh		Write DMA without Retry
20h	Read Sectors	CEh		Write Multiple FUA EXT
21h	Read Sectors without Retry	E0h		Standby Immediate
24h	Read Sectors EXT	E1h		Idle Immediate
25h	Read DMA EXT	E2h		Standby
27h	Read Native Max Address EXT	E3h		Idle
29h	Read Multiple EXT	E4h		Read Buffer
2Fh	Read Log EXT	E5h		Check Power Mode
30h	Write Sectors	E6h		Sleep
31h	Write Sectors without Retry	E7h		Flush Cache
34h	Write Sectors EXT	E8h		Write Buffer
35h	Write DMA EXT	E9h		READ BUFFER DMA
37h	Set Native Max Address EXT	EAh		Flush Cache EXT
38h	CFA Write Sectors Without Erase	EBh		Write Buffer DMA
39h	Write Multiple EXT	ECh		Identify Device
3Dh	Write DMA FUA EXT	EFh		Set Features
3Fh	Write Long EXT	EFh 02	2h	Enable volatile write cache
40h	Read Verify Sectors	EFh 03	3h	Set transfer mode
41h	Read Verify Sectors without Retry	EFh 05	5h	Enable the APM feature set
42h	Read Verify Sectors EXT	EFh 10)h	Enable use of SATA features et



44h Zero EXT EFh 10h 02h Enable DMA Setup FIS Auto-Activate optimization 45h Write Uncorrectable EXT EFh 10h 03h Enable Device-initiated interface power state (DIPM) transitions 47h Read Log DMA EXT EFh 10h 06h (SSP) 57h Write Log DMA EXT EFh 10h 07h Enable Device Automatic Partial to Slumber transitions 60h Read FPDMA Queued EFh 10h 09h Enable Device Sleep 61h Write FPDMA Queued EFh 55h Disable read look-ahead 770-77h Seek EFh 66h Disable reverting to power-on defaults 90h Execute Device Diagnostic EFh 82h Disable the APM feature set 91h Initialize Device Parameters EFh 85h Disable use of SATA feature set 92h Download Microcode EFh 90h Disable DMA Setup FIS Auto-Activate optimization 80h SMART EFh 90h 03h Disable DMA Setup FIS Auto-Activate optimization 80h SMART EFh 90h 07h Sibable Software Settings Preservation (SSP) 80h D1h SMART READ DATA EFh 90h 07h Disable Device-initiated interface power state (DIPM) transitions 80h D2h SMART READ ATRIBUTE THRESHOLDS EFh 90h 07h Disable Device-initiated interface power state (DIPM) transitions 80h D3h SMART READ ATRIBUTE THRESHOLDS EFh 90h Disable Device-initiated interface power state (DIPM) transitions 80h D3h SMART READ ATRIBUTE THRESHOLDS EFh 90h Disable Device-initiated interface power state (DIPM) transitions 80h D3h SMART READ BATA EFh 90h D9h Disable Device-initiated interface power state (DIPM) transitions 80h D3h SMART READ BATA EFh 90h D9h Disable Device-initiated interface power state (DIPM) transitions 80h D3h SMART ENABLE/DISABLE ATTRIBUTE FFh 90h D9h Disable Device Sleep 80h D3h SMART ENABLE/DISABLE ATTRIBUTE FFh 90h D9h Disable Device Sleep 80h D3h SMART ENABLE DEPARTIONS F3h Security Freeze Lock 80h D3h SMART ENABLE DERRATIONS F3h Security Freeze Lock 80h D8h SMART ENABLE OPERATIONS F3h Security Freeze Lock 80h D8h SMART ENABLE OPERATIONS F3h Security Disable Password 80h D8h SMART ENABLE OPERATIONS F3h Security Disable Password 80h D8h SMART ENABLE OPERATIONS F3h Security Disable Password 80h D8h SMART ENABLE OPERATIONS F3h Se		1			1		
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47h Read Log DMA EXT EFh 10h 05h Enable Software Settings Preservation (SSP) 57h Write Log DMA EXT EFh 10h 07h Enable Device Automatic Partial to Slumber transitions 60h Read FPDMA Queued EFh 55h Disable reverting to power-on defaults 90h Execute Device Diagnostic EFh 82h Disable reverting to power-on defaults 90h Execute Device Diagnostic EFh 85h Disable reverting to power-on defaults 91h Initialize Device Parameters EFh 85h Disable to set SATA feature set 92h Download Microcode EFh 90h 02h Disable use of SATA feature set 93h Download Microcode DMA EFh 90h 02h Disable Device Parameters EFH 90h 05h SMART READ DATA EFH 90h 06h Disable Software Settings Preservation (SSP) 80h D1h SMART READ DATA EFH 90h 07h Disable Device Initiated interface power state (DIPM) transitions 80h D2h SMART READ ATTRIBUTE THRESHOLDS EFH 90h 07h Disable Device Automatic Partial to Slumber transitions 80h D2h SMART ENABLE/DISABLE ATTRIBUTE EFH AAH Enable Pevice Sleep 80h D3h SMART SAVE ATTRIBUTE VALUES EFH AAH Enable read look-ahead 80h D4h SMART EXECUTE OFF-LINE IMMEDIATE EFH SCH SAMART SAVE ATTRIBUTE LOG F1 SAMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Frase Prepare 80h D8h SMART ENABLE OPERATIONS F3h Security Disable Password 81h Device Configuration F8h Read Native Max Address 84h Sanitize F9h O1h SET MAXLOCK 85h Writ							<u> </u>
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Slumber transitions Slumber transitions	5	7h	Write Log DMA EYT	EEh	10h	07h	Enable Device Automatic Partial to
G1h Write FPDMA Queued EFh S5h Disable read look-ahead		/11	WITTE EDG DIVIA EXT	EFII	1011	0711	Slumber transitions
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90h Execute Device Diagnostic EFh 82h Disable volatile write cache 91h Initialize Device Parameters EFh 85h Disable the APM feature set 92h Download Microcode EFh 90h Disable use of SATA feature set 93h Download Microcode DMA EFh 90h 02h Disable DMA Setup FIS Auto-Activate optimization Disable Device-initiated interface power state (DIPM) transitions Disable Device-initiated interface power state (DIPM) transitions Disable Device-initiated interface power state (DIPM) transitions Disable Software Settings Preservation (SSP) Disable Device Automatic Partial to Slumber transitions SMART READ ATTRIBUTE THRESHOLDS EFh 90h 07h Disable Device Automatic Partial to Slumber transitions SMART ENABLE/DISABLE ATTRIBUTE EFh 90h 09h Disable Device Sleep Disable Power-on defaults Disable SMART EXECUTE OFF-LINE IMMEDIATE EFh CCh Enable reverting to power-on defaults Disable SMART ENABLE OPERATIONS F3h Security Security Unlock Disable SMART ENABLE OPERATIONS F3h Security Erase Prepare Disable Device Sleep SMART ENABLE OPERATIONS F3h Security Erase Unit Security Erase Unit SMART ENABLE/DISABLE AUTOMATIC OFF-LINE Security Disable Password Disable Device Configuration F8h Security Disable Password Disable Device Configuration F8h Security Disable Password Disable Device Device Sleep Disable Device Device Device Sleep Disable Device Dev	6	1h	Write FPDMA Queued	EFh	5.	5h	Disable read look-ahead
91h	70h	-7Fh	Seek	EFh	6	6h	Disable reverting to power-on defaults
92h Download Microcode EFh 90h Disable use of SATA feature set 93h Download Microcode DMA EFh 90h 02h Disable DMA Setup FIS Auto-Activate optimization Disable Device-initiated interface power state (DIPM) transitions Disable Software Settings Preservation (SSP) Disable Software Settings Preservation (SSP) Disable Software Settings Preservation (SSP) Disable Device Automatic Partial to Slumber transitions Disable Device Sleep Dis	9	0h	Execute Device Diagnostic	EFh	8	2h	Disable volatile write cache
Download Microcode DMA EFh 90h 02h Disable DMA Setup FIS Auto-Activate optimization	9	1h	Initialize Device Parameters	EFh	8	5h	Disable the APM feature set
BOH DOH SMART READ DATA EFH 90h 05h Disable Device-initiated interface power state (DIPM) transitions BOH DOH SMART READ DATA EFH 90h 06h Disable Software Settings Preservation (SSP) BOH DOH SMART READ ATTRIBUTE THRESHOLDS EFH 90h 07h Disable Device Automatic Partial to Slumber transitions BOH D2h SMART ENABLE/DISABLE ATTRIBUTE AUTOSAVE EFH 90h 09h Disable Device Sleep BOH D3h SMART SAVE ATTRIBUTE VALUES EFH AAH Enable read look-ahead BOH D4h SMART EXECUTE OFF-LINE IMMEDIATE EFH CCH Enable reverting to power-on defaults BOH D5h SMART READ LOG F1h Security Set Password BOH D6h SMART WRITE LOG F2h Security Unlock BOH D8h SMART ENABLE OPERATIONS F3h Security Erase Prepare BOH D9h SMART DISABLE OPERATIONS F4h Security Freeze Lock BOH DAH SMART ENABLE OPERATIONS F5h Security Freeze Lock BOH DBH SMART ENABLE OPERATIONS F5h Security Disable Password B1h Device Configuration F8h Read Native Max Address B4h Sanitize F9h O1h SET MAX SET PASSWORD C5h Write Multiple F9h O2h SET MAX UNLOCK	9	2h	Download Microcode	EFh	9	0h	Disable use of SATA feature set
BOh DOH SMART READ DATA EFH 90h 03h Disable Device-initiated interface power state (DIPM) transitions BOh DOH SMART READ DATA EFH 90h 06h Disable Software Settings Preservation (SSP) BOH DOH SMART READ ATTRIBUTE THRESHOLDS EFH 90h 07h Disable Device Automatic Partial to Slumber transitions BOH DOH SMART ENABLE/DISABLE ATTRIBUTE AUTOSAVE EFH 90h 09h Disable Device Sleep BOH DOH SMART SAVE ATTRIBUTE VALUES EFH AAH Enable read look-ahead BOH DOH SMART EXECUTE OFF-LINE IMMEDIATE EFH CCH Enable reverting to power-on defaults BOH DOH SMART WRITE LOG FOH SMART WRITE LOG FOH SECURITY SET PASSWORD BOH DOH SMART DISABLE OPERATIONS FOH SECURITY Erase Prepare BOH DOH SMART ENABLE OPERATIONS FOH SECURITY Erase Unit BOH DOH SMART RETURN STATUS FOH SECURITY Freeze Lock BOH DOH SMART ENABLE/DISABILE AUTOMATIC OFF-LINE BOH DOH SET MAX Address BOH SET MAX SET PASSWORD CSH Write Multiple FOH OOH SET MAX UNLOCK	9:	3h	Download Microcode DMA	EFh	90h	02h	-
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C5h Write Multiple F9h O2h SET MAXLOCK C6h Set Multiple Mode F9h O3h SET MAX UNLOCK	B			F9h			Set Max Address
C6h Set Multiple Mode F9h 03h SET MAX UNLOCK	C	4h	Read Multiple	F9h 01h		01h	SET MAX SET PASSWORD
	C	5h	Write Multiple	F9ł	n	02h	SET MAXLOCK
C8h Read DMA F9h O4h SET MAX FREEZE LOCIK	С	6h	Set Multiple Mode			03h	SET MAX UNLOCK
	C	·		F9ł	1	04h	SET MAX FREEZE LOCIK



STANDARDS & REFERENCES

The following table is to list out the standards that have been adopted for designing the product.

STANDARD USED	ACRONYM/SOURCE
RoHS	Restriction of Hazardous Substances Directive; please contact us for further information
Serial ATA Revision 3.2	http://www.sata-io.org
ATA-8 spec	http://www.t13.org
CE	Consumer electronics certification; please contact us for further information.

SAFETY PRECAUTIONS

Do not bend, crush, drop or place heavy objects on top of the Product. Do not use tweezers, pliers or similar items that could damage the Product. Take particular care when inserting or removing the Product. Stop using the Product when the Product does not work properly. Failure to follow these instructions could result in fire, damage to the Product and/or other property, and/or personal injury including burns and electric shock.

Keep out of reach of small children. Accidental swallowing may cause suffocation or injury. Contact a doctor immediately if you suspect a child has swallowed the Product.

Do not directly touch the interface pins, put them in contact with metal, strike them with hard objects or cause them to short. Do not expose to static electricity.

Do not disassemble or modify the Product. This may cause electric shock, damage to the Product or fire.



NOTES ON USAGE

The Product contains nonvolatile semiconductor memory. Do not use the Product in accordance with a method of usage other than that written in the manual. This may cause the destruction or loss of data.

To protect against accidental data loss, you should back up your data frequently on more than one type of storage media. Wilk Elektronik S.A. assumes no liability for destruction or loss of data recorded on the Card for any reason.

When used over a long period of time or repeatedly, the reading, writing and deleting capabilities of the Product will eventually fail, and the performance speed of the Product may decrease below the original speed specific to the Product's applicable class.

If the Product is to be transferred or destroyed, note that the data it contained may still be recoverable unless it is permanently deleted by third-party deletion software or similar means beforehand.

Product is intended for use in general electronics applications and selected industrial applications and any other specific applications as expressly stated in this document. Product is neither intended nor warranted for use in equipment or systems where failure may cause loss of human life, bodily injury, serious property damage or serious public impact ("Unintended Use"). Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment or equipment used to control combustions or explosions. Do not use Product for Unintended Use unless specifically permitted in this document.

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